

# Highly Efficient and Reliable PV Inverter Configuration for Leakage current elimination in a Grid connect PV System

<sup>1</sup>J Sai Vathsav, UG Student, Department of EEE, Pragati Engineering college, Surampalem

vathsav28@gmail.com

<sup>2</sup>Sattiraju Dattatreya, UG Student, Department of EEE, Pragati Engineering college, Surampalem

sattirajudattatreya@gmail.com

<sup>3</sup>Thotakura Krishna Babu Naidu, UG Student, Department of EEE, Pragati Engineering college, Surampalem

krishnababut1999@gmail.com

<sup>4</sup>V Naga Saikiran, UG Student, Department of EEE, Pragati Engineering college, Surampalem

sai93980@gmail.com

<sup>5</sup>V Yadava Ramesh, UG Student, Department of EEE, Pragati Engineering college, Surampalem

v.yadavranesh225@gmail.com

**Abstract:** Standalone PV Systems are very useful in providing Power to the remote located electrical loads, but the stand alone system cannot supply power during all atmospheric conditions. By connecting the PV system to the local grid it can be made more reliable than the stand alone system. The grid connected PV Systems are used to provide electrical energy to the local loads and for the exchange power with utility grids. PV systems still face major difficulties and may pose some adverse effects to the system, such as overloading of the feeders, harmonics, high investment cost, low efficiency, and low reliability. In this paper, a comparative analysis of seven levels of MLI's and leakage current elimination is presented. Control scheme based on Sinusoidal Pulse Width Modulation (SPWM) is adopted due to its ease of implementation. More number of levels results in reduced THD and nearly sinusoidal output. Simulation is performed using Matlab/Simulink.

**Keywords:** *Photovoltaic Cell, Total Harmonic Distortion(THD), PWM (Pulse Width Modulation).*

## I. INTRODUCTION

Utilization of renewable energy resources is the demand of today and the necessity of tomorrow. With advancement in power electronic technology, the solar photovoltaic energy has been recognized as an important renewable energy resource because it is clean, abundant and pollution free. The PV power supplied to the utility grid is gaining more and more visibility, while the world's power demand is increasing [1]. Not many PV systems have so far been placed into the grid due to the relatively high cost, compared with more traditional energy sources such as oil, gas, coal, nuclear, hydro, and wind. Solid-state inverters have been shown to be the enabling technology for putting PV systems into the grid. The price of the PV modules were in the past the major contribution to the cost of these systems. A downward tendency is now seen in the price for the PV modules due to a massive increase in the production capacity of PV modules [2]. The cost of the grid-connected inverter is, therefore, becoming more visible in the total system price. A cost reduction per inverter watt is, therefore, important to make PV-generated power more attractive. Solar-electric-energy demand has grown consistently by

20%–25% per annum over the past twenty years, which is mainly due to the decreasing prices of the PV products, an increasing efficiency of solar cells and manufacturing technology improvements [3]. PV inverter, the heart of the grid connected and standalone PV system, is used to convert dc power obtained from PV modules into ac power to be fed into the grid. Improving the output waveform of the inverter reduces its respective harmonic content and hence the size of the filter used and the level of electromagnetic interference (EMI) generated by switching operation of the inverter. In recent years, multilevel inverters have become more attractive for researchers and manufacturers due to their advantages over conventional three-level pulse width-modulated (PWM) inverters. They offer improved output waveforms, smaller filter size, and lower EMI, lower total harmonic distortion (THD).

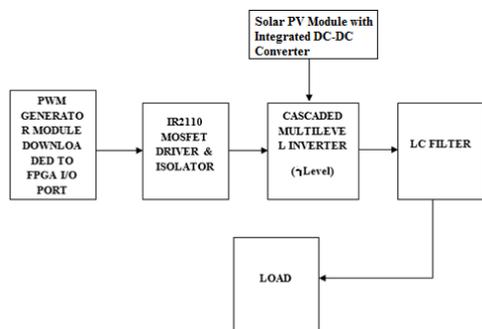


Fig1 shown above is the basic schematic model of the standalone PV system. Multilevel inverter are designed based on basic inverter model [4] which is shown in Fig.2 and its output is shown in Fig.3. Multilevel inverters can be used to interface with renewable energy and distributed energy resources because several batteries, fuel cells, PV cells, wind turbines, and micro turbines can be connected through a multilevel inverter to supply a load or the ac grid without

voltage balancing problems [5]. Generally, the battery requires a large capacity transformer for connecting to the power system. As a result, the whole system becomes large, heavy and has low efficiency. Recently, a transformer less battery energy storage system based on a cascaded multilevel inverter has been proposed [6]. A cascaded multilevel inverter has a simple structure that has promoted its application at megavolt level. By using it, the whole system can be reduced in size, weight, and cost. The cascaded multilevel inverter synthesizes a desired voltage from several independent sources of DC voltage, which may be obtained from batteries, fuel cells or PV cells [7]. This DC voltage must be filtered, not only clean but also well regulated. However, if the current source is directly loaded, then the output voltage will change. Therefore, it need a DC voltage control, thus the equipment can work according to its ability.

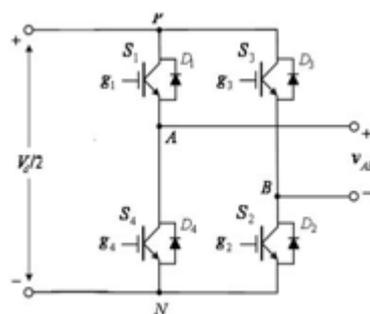


Fig.2. Basic Inverter.

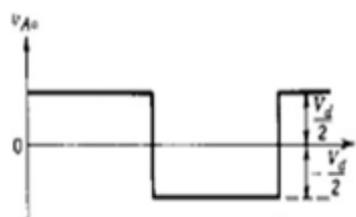


Fig.3. Output waveform.

### III Photovoltaic system

A solar cell, or photovoltaic cell, is an electrical device that converts the energy

of light directly into electricity by the photovoltaic effect, which is a physical and chemical phenomenon. It is a form of photoelectric cell, defined as a device whose electrical characteristics, such as current, voltage, or resistance, vary when exposed to light. Solar cells are the building blocks of photovoltaic modules, otherwise known as solar panels.

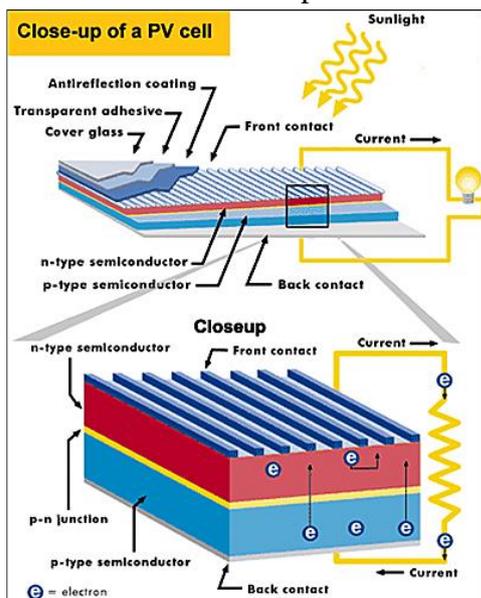


Figure4: Solar cell

Photons of light with energy higher than the band-gap energy of PV material can make electrons in the material break free from atoms that hold them and create electron and hole pairs. These electrons however, will soon fall back into holes causing charge carriers to disappear. If a nearby electric field is provided, those in the conduction band can be continuously swept away from holes toward a metallic contact where they will emerge as an electric current. The electric field within the semiconductor itself at the junction between two regions of crystals of different type, called a p-n junction. The PV cell has electrical contacts on its top and bottom to capture the electrons.

When the PV cell delivers power to the load, the electrons flow out of the n-side into the connecting wire, through the load, and back flows in the opposite direction from electrons.

#### IV Modelling of PV Array

PV arrays are built up with combined series/parallel combinations of PV solar cells, which are usually represented by a simplified equivalent circuit model such as the one given in Figure 3.

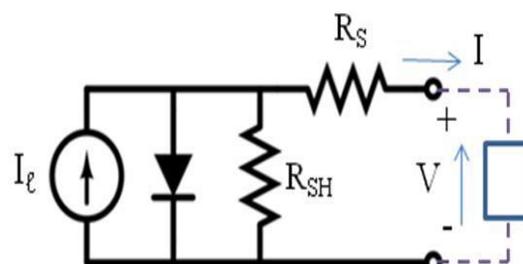


Figure5: Equivalent circuit for a PV cell

The PV cell output voltage is a function of the photocurrent that mainly determined by load current depending on the solar Irradiation level during the operation.

$$V_c = \frac{AkT_c}{e} \ln \left( \frac{I_{ph} + I_0 - I_c}{I_0} \right) - R_s I_c$$

Where the symbols are defined as follows:

- e: electron charge ( $1.602 \times 10^{-19}$  C).
- k: Boltzmann constant ( $1.38 \times 10^{-23}$  J/PoPK).
- I<sub>c</sub>: cell output current, A.
- A: curve fitting factor used to adjust the I-V characteristics of the cell
- I<sub>ph</sub>: photocurrent, function of irradiation level and junction temperature (5 A).
- I<sub>0</sub>: reverse saturation current of diode (0.0002 A).
- R<sub>s</sub>: series resistance of cell (0.001 Ω).
- T<sub>c</sub>: reference cell operating temperature (20 °C).
- V<sub>c</sub>: cell output voltage, V.

If the temperature and solar irradiation levels change, the voltage and current outputs of the PV array will follow this change. Hence, the effects of the changes in temperature and solar irradiation levels should also be included in the PV array model.

**V. BASIC STRUCTURE**

The cascaded H-Bridge inverter is composed of several H-bridge converters in cascade connection as given in Fig. 6. This topology uses separate dc sources and high quality multilevel waveforms can be achieved with comparatively lesser components. The voltage levels at the output  $v_m'$  in a cascade inverter is defined by  $m = 2n+1$ , where  $n'$  is the number of dc sources. Hence '4n' power switches are required to synthesize all possible combinations. This results in harmonic reduction in the synthesized current which reduces the size of the output filters [11].

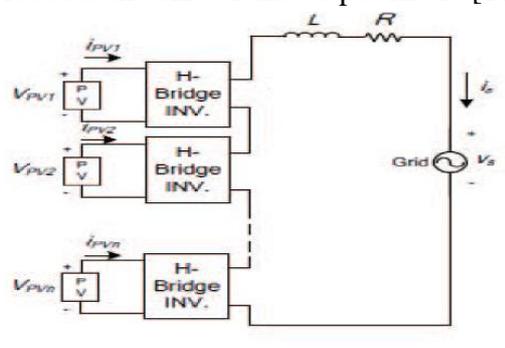


Fig. 6. Structure of a Cascaded H Bridge MLI for a grid connected PV system

A cascaded multilevel inverter uses set of series connected full-bridge inverters with separate DC sources in a modular setup to create the stepped waveform. A full bridge inverter is in itself a 3 level cascaded multilevel inverter and every module added in cascade to that extends the inverter with two more voltage levels,

which then increases the number of steps in the waveform. A typical 5-level for a single phase leg is shown below. One can observe from here that it requires 8 switches. For a 7-level inverter model, it uses 12 switches, whereas the proposed model uses only 9.

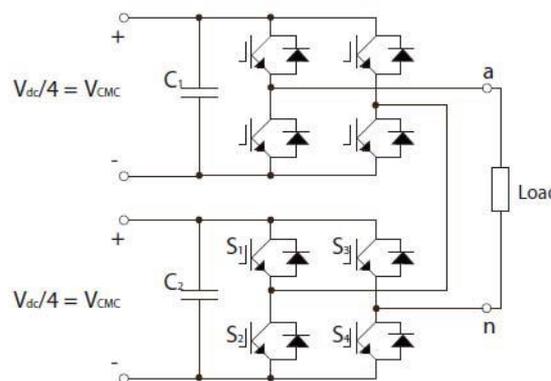


Figure 7. Typical 5-level cascaded multilevel inverter circuit.

The traditional two or three levels inverter does not completely eliminate the unwanted harmonics in the output Waveform. Therefore, using the multilevel inverter as an alternative to traditional PWM inverters is investigated. Cascaded H-bridge multilevel inverters typically use IGBT switches.

**VI. Simulation Results**

The simulation model of Reduced Switches Cascaded 7- level MLI of designed using MATLAB/SIMULINKS Software. The gating signals for the inverter are generated by using multicarrier modulation technique. The circuit was simulated with RL load. The simulation parameters are shown in below table 2 here in this section simulation results and observations of Photovoltaic Application based 7- level Cascaded MLI with Reduced No. of Switches is presented. The different parameters (like voltage, current, THD) are observed.

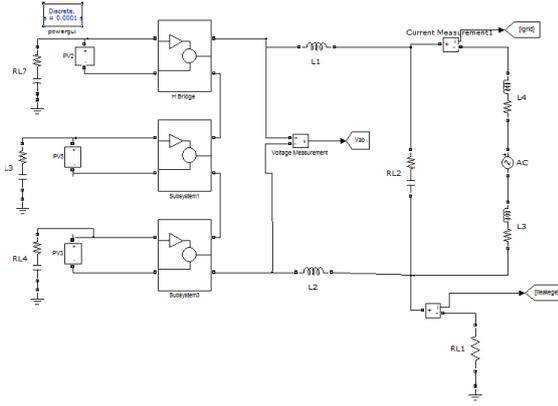


Figure 8 shows the MATLAB Model of Photovoltaic based 7- level Cascaded MLI with reduced no. of switches

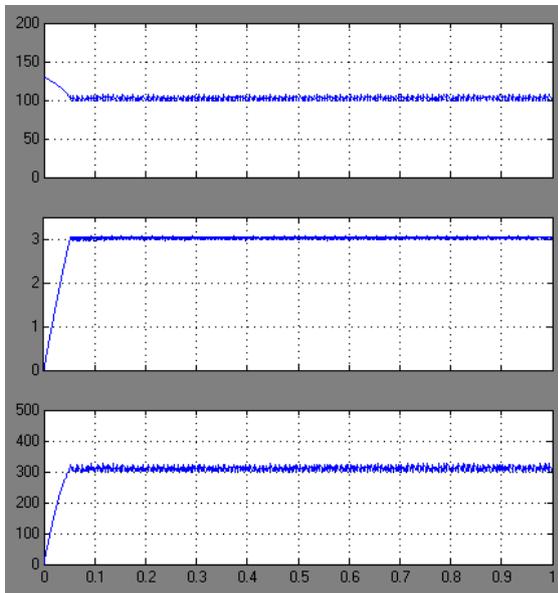


Figure 9 Simulation waveforms of the PV Panel 1 Voltage (V), Current (A), and Power (W).

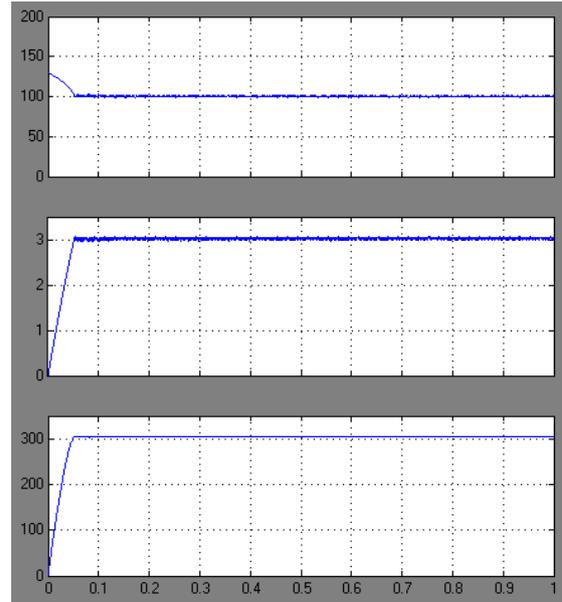


Figure 10 Simulation waveforms of the PV Panel 2 Voltage (V), Current (A), and Power (W).

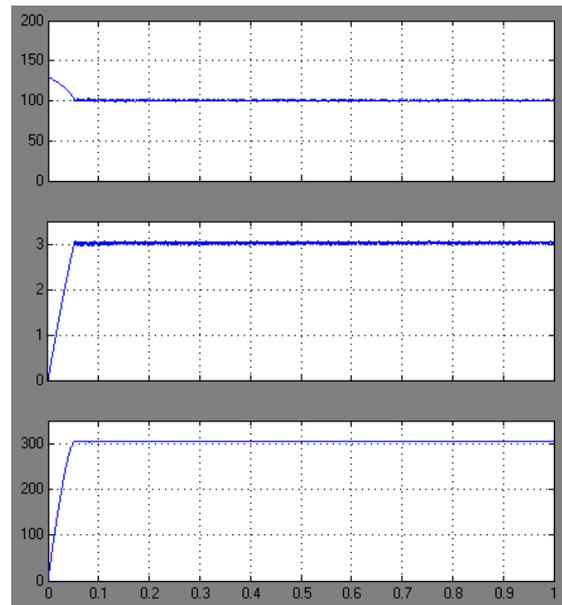


Figure 11 Simulation waveforms of the PV Panel 3 Voltage (V), Current (A), and Power (W).

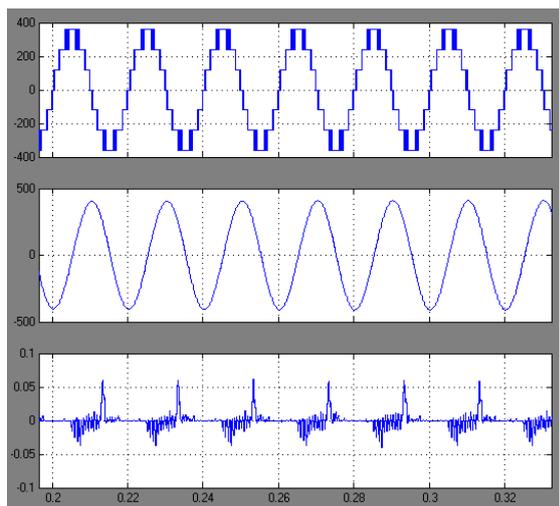


Figure 13 Simulation waveforms of the Output Voltage (V), Output Current (A), and Leakage Current (A).

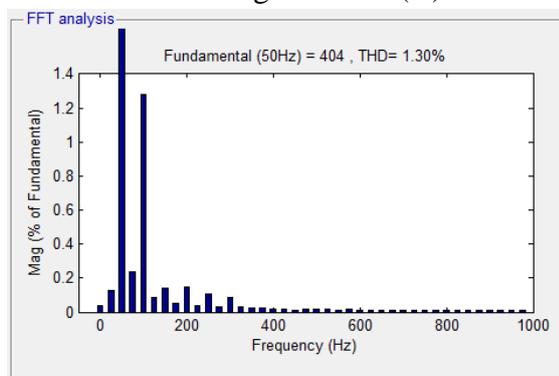


Figure 14 Total Harmonic Distortion of the Output Inverter Current in %

## VII. Conclusion

Photovoltaic based applications are increasing day by day. Since most of the electrical application are in AC, so some efficient Power Electronics DC to AC converters are required for converting photovoltaic DC output to AC. In this paper a Photovoltaic application based 7-level Cascaded MLI has been shown to produce an increased stepped output with less number of semiconductor switches, and due to which controlling the overall circuit becomes less complex, the size and installation area reduces. A single phase Photovoltaic Array fed Cascaded H-Bridge MLI with Reduced number of Semiconductor Switches is simulated in

MATLAB / SIMULINK environment. It is inferred with the help of simulation results the Multicarrier PWM techniques produced a lower THD than with PWM technique involving the comparison between triangular and constant values.

## REFERENCES

- [1] Y. Tang, W. Yao, P.C. Loh and F. Blaabjerg, "Highly Reliable Transformer less Photovoltaic Inverters With Leakage Current and Pulsating Power Elimination," *IEEE Trans. Ind. Elect.*, vol. 63, no. 2, pp. 1016-1026, Feb. 2016.
- [2] W. Li, Y. Gu, H. Luo, W. Cui, X. He and C. Xia, "Topology Review and Derivation Methodology of Single-Phase Transformer less Photovoltaic Inverters for Leakage Current Suppression," *IEEE Trans. Ind. Elect.*, vol. 62, no. 7, pp. 4537-4551, July 2015.
- [3] J. Ji, W. Wu, Y. He, Z. Lin, F. Blaabjerg and H. S. H. Chung, "A Simple Differential Mode EMI Suppressor for the LLCL-Filter-Based Single-Phase Grid-Tied Transformer less Inverter," *IEEE Trans. Ind. Elect.*, vol. 62, no. 7, pp. 4141-4147, July 2015.
- [4] Y. Bae and R.Y.Kim, "Suppression of Common-Mode Voltage Using a Multicentral Photovoltaic Inverter Topology With Synchronized PWM," *IEEE Trans. Ind. Elect.*, vol. 61, no. 9, pp. 4722-4733, Sept. 2014.
- [5] N. Vazquez, M. Rosas, C. Hernandez, E. Vazquez and F. J. Perez-Pinal, "A New Common-Mode Transformer less Photovoltaic Inverter," *IEEE Trans. Ind. Elect.*, vol. 62, no. 10, pp. 6381-6391, Oct. 2015.
- [6] G. Buticchi, E. Lorenzani and G. Franceschini, "A Five-Level Single-Phase Grid-Connected Converter for Renewable

- Distributed Systems," *IEEE Trans. Ind. Elect.*, vol.60, no.3, pp.906-918, March 2013.
- [7] N.A. Rahim and J. Selvaraj, "Multistring Five-Level Inverter With Novel PWM Control Scheme for PV Application," *IEEE Trans. Ind. Elect.*, vol.57, no.6, pp.2111-2123, June 2010.
- [8] M. Cavalcanti, K. De Oliveira, A. M. de Farias, F. Neves, G. Azevedo and F. Camboim, "Modulation Techniques to Eliminate Leakage Currents in Transformer less Three-Phase Photovoltaic Systems," *IEEE Trans. Ind. Elect.*, vol. 57, no. 4, pp. 1360-1368, April 2010.
- [9] L. Zhang, K. Sun, L. Feng, H. Wu and Y. Xing, "A Family of Neutral Point Clamped Full-Bridge Topologies for Transformerless Photovoltaic Grid-Tied Inverters," *IEEE Trans. Power Elect.*, vol.28, no.2, pp.730-739, Feb. 2013.
- [10] M. Islam and S. Mekhilef, "H6-type transformerless single-phase inverter for grid-tied photovoltaic system," *IET Elect. Power Elect.*, vol.8, no.4, pp.636-644, 2015.
- [11] H.F. Xiao, K. Lan and L. Zhang, "A Quasi-Unipolar SPWM Full-Bridge Transformerless PV Grid-Connected Inverter with Constant Common-Mode Voltage," *IEEE Trans. Power Elect.*, vol.30, no.6, pp.3122-3132, June 2015.
- [12] B.Ji, J. Wang and J. Zhao, "High-Efficiency Single-Phase Transformerless PV H6 Inverter With Hybrid Modulation Method," *IEEE Trans. Ind. Elect.*, vol.60, no.5, pp.2104-2115, May 2013.
- [13] G. Buticchi, D. Barater, E. Lorenzani, C. Concari and G. Franceschini, "A Nine-Level Grid-Connected Converter Topology for Single-Phase Transformerless PV Systems," *IEEE Trans. Ind. Elect.*, vol.61, no.8, pp.3951-3960, Aug. 2014.
- [14] F. Hong, J. Liu, B. Ji, Y. Zhou, J. Wang and C. Wang, "Single Inductor Dual Buck Full-Bridge Inverter," *IEEE Trans. Ind. Elect.*, vol.62, no.8, pp.4869-4877, Aug. 2015.
- [15] S. V. Araujo, P. Zacharias and R. Mallwitz, "Highly Efficient Single-Phase Transformer less Inverters for Grid-Connected Photovoltaic Systems," *IEEE Trans. Ind. Elect.*, vol. 57, no. 9, pp. 3118-3128, Sept. 2010.
- [16] O. Lopez, R. Teodorescu and J. D. Gandoy, "Multilevel transformerless topologies for single-phase grid-connected converters," APEC 2006, pp.5191-5196, Nov. 2006.
- [17] O. Lopez, R. Teodorescu, F. Freijedo and J.D. Gandoy, "Leakage current evaluation of a single-phase Transformerless PV inverter connected to the grid," APEC 2007, pp.907-912, March 2007.
- [18] O. Lopez, F.D. Freijedo, A.G. Yepes, P. F. Comesaa, J. Malvar, R. Teodorescu and J. D. Gandoy, "Eliminating Ground Current in a Transformerless Photovoltaic Application," *IEEE Trans. Energy Conv.*, vol.25, no.1, pp.140-147, March 2010.
- [19] G. Vazquez, P.R.M. Rodriguez, G. Escobar, J.M. Sosa and R.M. Mendez, "A PWM method for single-phase cascade multilevel inverters to reduce leakage ground current in transformerless PV systems," (accepted for publication in International Transactions on Electrical Energy Systems, Wiley Publications).
- [20] M. Killi and S. Samanta, "Modified Perturb and Observe MPPT Algorithm for Drift Avoidance in Photovoltaic Systems,"

*IEEE Trans. Ind. Elect.*, vol. 62, no. 9, pp. 5549-5559, Sept. 2015.

[21] M. Hedayati and V. John, "Filter Configuration and PWM Method For Single Phase Inverters with Reduced Conducted EMI Noise," *IEEE Trans. Ind. App.*, vol. 51, no. 4, pp. 3236-3243, July-Aug. 2015.

[22] D. Barater, G. Buticchi, E. Lorenzani and C. Concari, "Active Common-Mode Filter for Ground Leakage Current Reduction in Grid-Connected PV Converters Operating With Arbitrary Power Factor," *IEEE Trans. Ind. Elect.*, vol.61, no.8, pp.3940-3950, Aug. 2014.

[23] T. Kerekes, R. Teodorescu, P. Rodriguez, G. Vazquez and E. Aldabas, "A New High-Efficiency Single-Phase Transformerless PV Inverter Topology," *IEEE Trans. Ind. Elect.*, vol.58, no.1, pp.184-191, Jan. 2011.

[24] T. Kerekes, R. Teodorescu, M. Liserre, C. Klumpner and M. Sumner, "Evaluation of Three-Phase Transformerless Photovoltaic Inverter Topologies," *IEEE Trans. Power Elect.*, vol. 24, no. 9, pp. 2202-2211, Sept. 2009.

[25] T. K. S. Freddy, N. A. Rahim, W. P. Hew and H. S. Che, "Modulation Techniques to Reduce Leakage Current in Three-Phase Transformerless H7 Photovoltaic Inverter," *IEEE Trans. Ind. Elect.*, vol. 62, no. 1, pp. 322-331, Jan. 2015.

[26] M. Islam and S. Mekhilef, "Efficient Transformerless MOSFET Inverter for Grid-Tied Photovoltaic System," *IEEE Trans. Power Elect.*, vol. 31, no. 9, pp. 6305-6316, Sept. 2016.

[27] F. Wu, X. Li, F. Feng and H.B.Gooi, "Modified Cascaded Multilevel Grid-Connected Inverter to Enhance European

Efficiency and Several Extended Topologies," *IEEE Trans. Ind. Elect.*, vol. 11, no. 6, pp. 1358-1365, Oct. 2013.

